

## RESUME

**N.VIKRAMAN**  
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Pattabiram, Chennai - 72.

### Career Objective

I strongly believe that “Constant aspiration will conquer all defects” and as a hard worker in a company, where I can contribute my talent for their success.

### Educational Qualification

GRADUATION	SPECIALIZATION	SCHOOL/ COLLEGE	BOARD/ UNIVERSITY	YEAR OF PASSING	%
Ph.D	Electronics and Communication Engineering	Saveetha School of Engineering	Saveetha University	Pursuing	-
M.E	Medical Electronics	Anna University, (CEG) Guindy	Anna University	2014	5.85 (CGPA)
B.E	Electronics and Communication Engineering	Oxford engg college Trichy.	Anna university	2006	67.61
H.S.C	Maths-Biology	MCNUPDA H.S.S Virudhunagar	State board	2001	76.8
S.S.L.C	S.S.L.C	MCNUPDA H.S.S. Virudhunagar	State board	1999	88

### Experience :

Assistant Professor of ECE Dept in Saveetha School of Engineering, Thandalam, Chennai – 602105, 07.04.2018 to 03.05.2019 – **1 Year.**

HOD & Assistant Professor of ECE Dept in Lord Ayyappa Institute of Engg & Tech, Kancheepuram – 631 605 – 24.02.2014 to 03.08.2016 - **2.6 Years.**

Assistant Professor of ECE Dept in John Bosco Engineering College, Tiruvallur – 602 001 – 13.01.2011 to 03.06.2013 – **2.6 Years.**

Lecturer of ECE Dept in TJ Institute of Technology, Karappakam, Chennai – 600 097 – 20.01.2010 to 12.12.2010 – **1 Year.**

Lecturer of ECE Dept in Bhajarang Engineering College, Vepampattu,

Tiruvallur – 602 024 – 01.11.2007 to 30.10.2009 – **2 Years.**

**Phd – Research Topic:** Design and Analysis of Malnutrition Identification and Implementation by using Recent Technologies – Domain: Digital Image Processing, Wireless Networks, Body Sensor Networks.

**Subject Taught:**

Digital Electronics & Digital Electronics Lab.  
Computer Networks & CN Lab.  
Digital Signal Processing.  
MicroProcessors & MicroController and its Lab.  
Basic Electrical & Electronics Engg, ESD Lab.  
Linear Integrated Circuits & LIC Lab.  
Computer Architecture & Organizations.  
Control Systems, Signals & Systems.  
High Speed Networks. VLSI & VLSI Lab.  
Digital Logic Circuits, Digital Principles and System Design.  
Medical Electronics. Television and Video Engineering.  
Nano Electronics & Nano Electronics Lab.

**Computer Skills:**

CCNA – Cisco Certified Network Associate.  
Languages : C, C++  
Diploma in Computer Application(DCA).  
Diploma in Computer Hardware and Networking Training.(DCHT).  
Diploma in Multimedia Training.

**Conference Attended:**

**International Conference - Patient Monitoring System – ICABP’13** at Bharathidasan University, Tiruchirappalli.

**National Conference - Malnutrition identification based on digital image processing techniques by utilising an optical sensor device** at Saveetha School of Engineering, Thandalam, Chennai.

**Workshop:**

Fundamentals of Image Enhancement, Feature Extraction Techniques & Implementation of image enhancement and segmentation techniques in MATLAB at Magna College of Engineering, Tiruvallur.

**Academic Project:**

The Following projects Successfully completed

“Remote Patient Monitoring System using Wireless Networks” – ME Project.

“Station Identification In Railways” – BE Project.

**Inplant Training:**

BSNL - Trichirappalli.

Ramco Cements (p) Ltd - R.R.Nagar, Virudhunagar.

**Paper Presentation:**

MEMS Technology - Rajeev Gandhi Memorial College of Engg & Tech , Hyderabad.

**Personal Profile:**

Name	:	N.Vikraman.
Date of Birth	:	28 <sup>th</sup> May 1984.
Father’s Name	:	Mr.L.Narayana Moorthi.
Mother’s Name	:	Mrs. N. Maha Lakshmi.
Permanent Address	:	3/104 RC street, Inam reddia patti, Virudhunagar – 626003.
Gender	:	Male.
Nationality	:	Indian.
Marital Status	:	Single.
Languages known	:	Read - Tamil,English. Write - Tamil,English. Speak - Tamil,English.

**Declaration:**

I here by declare that the above information furnished above is true to the best of my knowledge.

**(N.VIKRAMAN)**